System Diagram
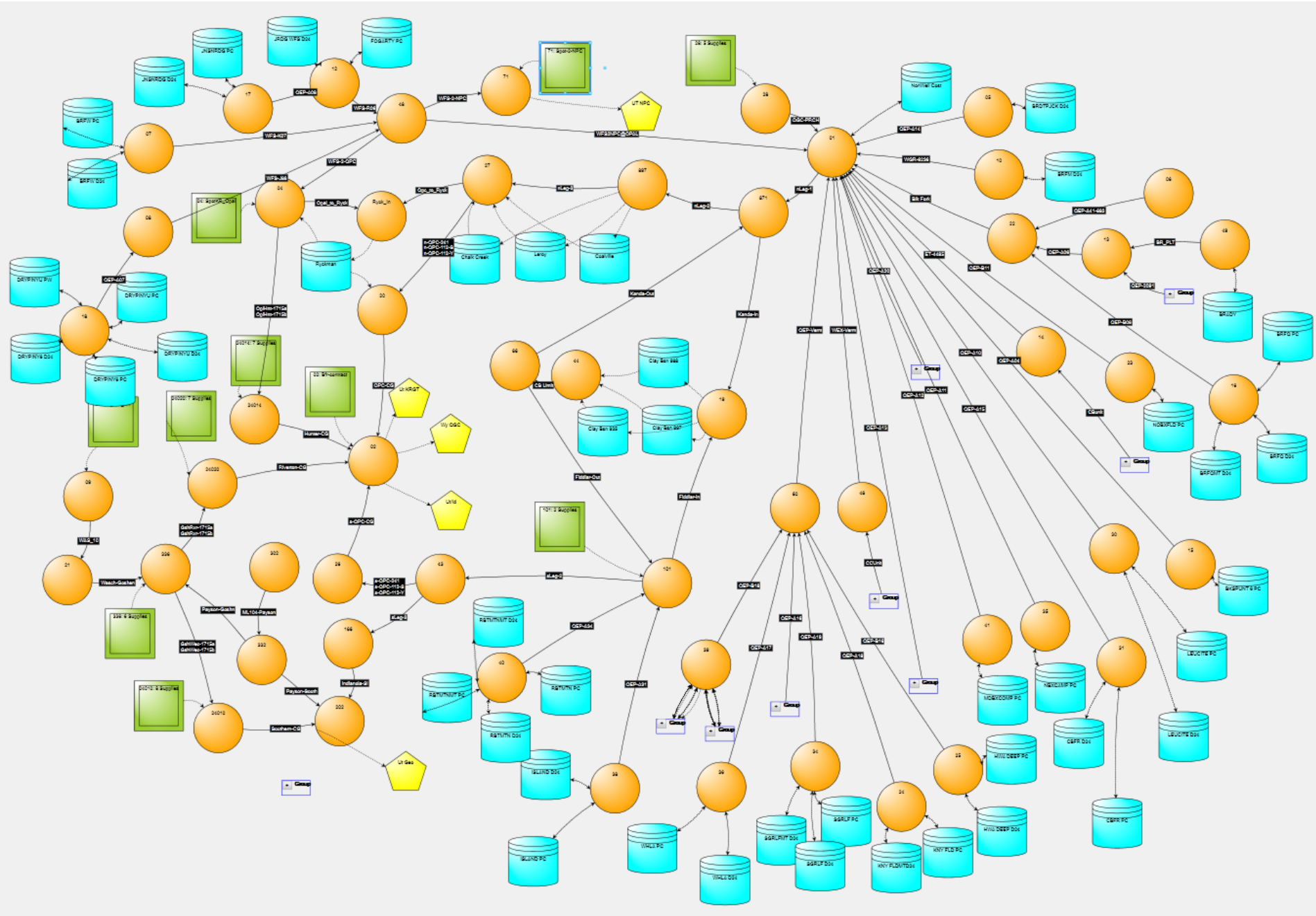
Exhibit 7.1